

Substitute for form 1449A/PTO		<b>Complete if Known</b>	
		Application Number	10/090,944
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing Date	March 4, 2002
		First Named Inventor	Paul E Luse
		Art Unit	2818 2189
		Examiner Name	Not yet assigned PEI/KAR
		Attorney Docket Number	42390P13502
Sheet	2	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
BYP	2	R. Russell, BIOS Basics Online!, June 5, 2001, one page	
BYP	3	A. Tischenko, "Zero-Channel RAID. The Economic Approach", Computer Review Online!, four pages	
BYP	4	Intel Corporation, "DK440LX Motherboard - Technical Product Specification", October 1997, 106 pages	
BYP	5	Intel Corporation, "MROMB Design Considerations Using the Intel 80303 I/O Processor - Application Note" September 2001, 15 pages	
BYP	6	Copy of PCT Search Report dated April 19, 2004	

Examiner Signature		Date Considered	5/4/05
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(use as many sheets as necessary)*

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Attorney Docket Number	42390P13502

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## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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Signature**

Date	Considered
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11/2/80	11/2/80
11/3/80	11/3/80
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<sup>2</sup> Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbol and kind of cited document.

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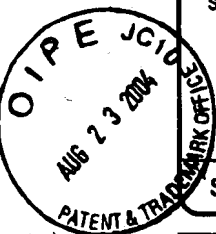
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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of

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Application Number

10/ 090, 944

Filing Date

3. 4. 02

First Named Inventor

Paul Luse

Art Unit

Examiner Name

Attorney Docket Number

42P13502

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
	1	Adaptec - Adaptec SCSI RAID 2000S Support	
	2	Adaptec - Adaptec SCSI RAID 2005S Support	DUPLICATE
	3	Adaptec - Motherboard Partners and Compatibility	
	4	Adaptec - Adaptec SCSI RAID 2005S Technical Specs	
	5	Adaptec - Adaptec Article: Zero Channel RAID Frequently Asked Questions	
	6	Intel Corp., Product Brief, Intel Server Board SCB2	
	7	Serial ATA: High Speed Serialized AT Attachment Revision 1.0, 29 August 2001	
	8	Super0, Super P3TDDR, User's Manual, Revision 1.0a	
	9	PCI Local Bus, PCI BIOS Specification, Revision 2.1 August 26, 1994	
	10	PCI Local Bus, PCT-to-PCI Bridge Architecture Specification, Revision 1.1, December 16, 1998	
	11	PCI Local Bus, PCI Local Bus Specification, Revision 2.2, December 18, 1998	
	12	PCI Local Bus, Mini PCI Specification, Revision 1.0, October 25, 1999	
	13	PCI Local Bus, PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a, July 24, 2000	
	14	PCI Local Bus, PCI Local Bus Specification, Revision 2.3, March 29, 2002	

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**Complete if Known**

Application Number	10/ 090, 944
Filing Date	3.4.02
First Named Inventor	Paul Lucci
Art Unit	
Examiner Name	
Attorney Docket Number	4201302

Sheet 4 of 4

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